APPLICATION DATA SHEET

Electronic Version 0.0.11

Stylesheet Version: 1.0   Attorney Docket Number: VIAP0010USA

Publication Filing Type: new-utility

Application Type: utility

Title of Invention: Integrated level shift and override circuit for a processor system

Attorney Docket Number: VIAP0010USA

Customer Number Attorney: 027765

Customer Number Correspondence Address: 027765

Continuity Data:

This application is a non-provisional of provisional 60/255,618 2000-12-13 US Pending

Foreign Priority:

US   2000-12-13   Priority Claimed

INVENTOR(s):

Primary Citizenship: R.O.C.

Given Name: Chia-Hsing

Family Name: Yu

Residence City: Taipei Hsien

Residence Country: TW

Address: No.17, Lane 33, Sec.1, Yung-An N. Rd., San-Chung City, Taipei Hsien, Taiwan, R.O.C.

                         Taipei Hsien ,

Primary Citizenship: R.O.C.

Given Name: Nai-Shung

Family Name: Chang

Residence City: Taipei Hsien

Address: 7F, No.26-6, Alley 14, Lane 10, Sec.3, Hsiu-Lang Rd.,
Chung-Ho City, Taipei Hsien, Taiwan, R.O.C.

Taipei Hsien ,